

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, and a switch, said semiconductor device characterized in that:~~

[[the]] ~~a~~ first transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a~~ second transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

a switch wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the switch; and

means for short-circuiting between the first terminal of the first transistor and the second terminal of the first transistor,

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor; and

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor[[: and]]

~~a means for short-circuiting between the first terminal of the first transistor and the second terminal of the first transistor is provided.~~

2. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, a first switch, and a second switch, said semiconductor device characterized in that:~~

[[the]] ~~a~~ first transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a~~ second transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a

second terminal; and

a first switch and a second switch wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the first switch[[;]],

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor;

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

wherein the first terminal of the first transistor and the second terminal of the first transistor are connected via the second switch.

3. (Currently Amended) The semiconductor device according to claim 1 [[or 2]],  
wherein the second terminal of the first transistor is connected to the first terminal of the second transistor via a third transistor.

4. (Currently Amended) The semiconductor device according to claim 1 [[or 2]], further  
comprising [[a]] means for short-circuiting between the first terminal of the second transistor and the second terminal of the second transistor.

5. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, and a switch, said semiconductor device characterized in that:~~

[[the]] ~~a first transistor comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a second transistor comprises~~ comprising a gate terminal, a first terminal, and a second terminal; and

a switch wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the switch; and

means for short-circuiting between the first terminal of the second transistor and the second terminal of the second transistor,

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor;

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor[[: and]]

~~a means for short-circuiting between the first terminal of the second transistor and the second terminal of the second transistor is provided.~~

6. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, a first switch, and a second switch, said semiconductor device characterized in that:~~

[[the]] ~~a first transistor comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a second transistor comprises~~ comprising a gate terminal, a first terminal, and a second terminal; and

a first switch and a second switch wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the first switch[[:]],

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor;

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

wherein the first terminal of the second transistor and the second terminal of the second transistor are connected via the second switch.

7. (Currently Amended) The semiconductor device according to claim 5 [[or 6]],  
wherein the second terminal of the first transistor is connected to the first terminal of the second transistor via a third transistor.

8. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, a first switch, a second switch, a third switch, and a wiring, said semiconductor device~~

~~characterized in that:~~

[[the]] ~~a~~ first transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a~~ second transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

a first switch, a second switch, and a third switch, wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the first switch; and

a wiring,

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor;

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor via a second switch; and

wherein the gate terminal of the second transistor is connected to the wiring via a third switch.

9. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, and a switch, said semiconductor device characterized in that:~~

[[the]] ~~a~~ first transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a~~ second transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal; and

a switch wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the switch; and

means for short-circuiting at least either of between the first terminal of the first transistor and the second terminal of the first transistor or between the first terminal of the second transistor and the second terminal of the second transistor,

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor;

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor[[: and]]

~~a means for short-circuiting at least either of between the first terminal of the first transistor and the second terminal of the first transistor or between the first terminal of the second transistor and the second terminal of the second transistor is provided.~~

10. (Currently Amended) A semiconductor device comprising ~~a first transistor, a second transistor, a first switch, and a second switch, said semiconductor device characterized in that:~~

[[the]] ~~a~~ first transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

[[the]] ~~a~~ second transistor ~~comprises~~ comprising a gate terminal, a first terminal, and a second terminal;

a first switch and a second switch wherein the gate terminal of the first transistor and the first terminal of the first transistor are connected via the first switch[[:]],

wherein the second terminal of the first transistor is connected to the first terminal of the second transistor;

wherein the gate terminal of the first transistor is connected to the gate terminal of the second transistor; and

wherein the second switch is disposed at least either of between the first terminal of the first transistor and the second terminal of the first transistor or between the first terminal of the second transistor and the second terminal of the second transistor.

11. (Currently Amended) The semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 8, 9, or 10,~~

wherein the first transistor and the second transistor have the same conductivity.

12. (Currently Amended) The semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 8, 9, or 10,~~

wherein a capacitor is provided and the gate terminal of the first transistor and one terminal of the capacitor are connected.

13. (Original) The semiconductor device according to claim 12,  
wherein the gate terminal of the first transistor is connected to one terminal of the capacitor, and the other terminal of the capacitor is connected to a second terminal of the second transistor.

14. (Currently Amended) The semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 8, 9, or 10,~~  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a current source circuit.

15. (Currently Amended) The semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 8, 9, or 10,~~  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a display element.

16. (Original) The semiconductor device according to claim 15,  
wherein the display element is an EL element.

17. (Currently Amended) A display device comprising the semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 9, 10, or 11.~~

18. (Original) An electronic apparatus comprising the display device according to claim 17.

19. (Currently Amended) A digital still camera comprising the semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 9, 10 or 11.~~

20. (Currently Amended) A personal computer comprising the semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 9, 10 or 11.~~

21. (Currently Amended) A video camera comprising the semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 9, 10 or 11.~~

22. (Currently Amended) A portable phone comprising the semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 9, 10 or 11.~~

23. (Currently Amended) An image reproducing apparatus provided with a recording medium, comprising the semiconductor device according to claim 1 ~~any one of claims 1, 2, 5, 6, 9, 10 or 11.~~

24. (New) The semiconductor device according to claim 2,  
wherein the second terminal of the first transistor is connected to the first terminal of the second transistor via a third transistor.

25. (New) The semiconductor device according to claim 2,  
further comprising means for short-circuiting between the first terminal of the second transistor and the second terminal of the second transistor.

26. (New) The semiconductor device according to claim 6,  
wherein the second terminal of the first transistor is connected to the first terminal of the second transistor via a third transistor.

27. (New ) The semiconductor device according to claim 2,  
wherein the first transistor and the second transistor have the same conductivity.
28. (New ) The semiconductor device according to claim 5,  
wherein the first transistor and the second transistor have the same conductivity.
29. (New ) The semiconductor device according to claim 6,  
wherein the first transistor and the second transistor have the same conductivity.
30. (New ) The semiconductor device according to claim 8,  
wherein the first transistor and the second transistor have the same conductivity.
31. (New ) The semiconductor device according to claim 9,  
wherein the first transistor and the second transistor have the same conductivity.
32. (New ) The semiconductor device according to claim 10,  
wherein the first transistor and the second transistor have the same conductivity.
33. (New) The semiconductor device according to claim 2,  
wherein a capacitor is provided and the gate terminal of the first transistor and one  
terminal of the capacitor are connected.
34. (New) The semiconductor device according to claim 5,  
wherein a capacitor is provided and the gate terminal of the first transistor and one  
terminal of the capacitor are connected.



35. (New) The semiconductor device according to claim 6,  
wherein a capacitor is provided and the gate terminal of the first transistor and one  
terminal of the capacitor are connected.

36. (New) The semiconductor device according to claim 8,  
wherein a capacitor is provided and the gate terminal of the first transistor and one  
terminal of the capacitor are connected.

37. (New) The semiconductor device according to claim 9,  
wherein a capacitor is provided and the gate terminal of the first transistor and one  
terminal of the capacitor are connected.

38. (New) The semiconductor device according to claim 10,  
wherein a capacitor is provided and the gate terminal of the first transistor and one  
terminal of the capacitor are connected.

39. (New) The semiconductor device according to claim 2,  
wherein the first terminal of the first transistor or the second terminal of the second  
transistor is connected to a current source circuit.

40. (New) The semiconductor device according to claim 5,  
wherein the first terminal of the first transistor or the second terminal of the second  
transistor is connected to a current source circuit.

41. (New) The semiconductor device according to claim 6,  
wherein the first terminal of the first transistor or the second terminal of the second  
transistor is connected to a current source circuit.

42. (New) The semiconductor device according to claim 8,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a current source circuit.

43. (New) The semiconductor device according to claim 9,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a current source circuit.

44. (New) The semiconductor device according to claim 10,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a current source circuit.

45. (New) The semiconductor device according to claim 2,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a display element.

46. (New) The semiconductor device according to claim 5,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a display element.

47. (New) The semiconductor device according to claim 6,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a display element.

48. (New) The semiconductor device according to claim 8,  
wherein the first terminal of the first transistor or the second terminal of the second transistor is connected to a display element.

49. (New) The semiconductor device according to claim 9,  
wherein the first terminal of the first transistor or the second terminal of the second  
transistor is connected to a display element.

50. (New) The semiconductor device according to claim 10,  
wherein the first terminal of the first transistor or the second terminal of the second  
transistor is connected to a display element.

51. (New) A display device comprising the semiconductor device according to claim 2.

52. (New) A display device comprising the semiconductor device according to claim 5.

53. (New) A display device comprising the semiconductor device according to claim 6.

54. (New) A display device comprising the semiconductor device according to claim 9.

55. (New) A display device comprising the semiconductor device according to claim 10.

56. (New) A digital still camera comprising the semiconductor device according to claim  
2.

57. (New) A digital still camera comprising the semiconductor device according to claim  
5.

58. (New) A digital still camera comprising the semiconductor device according to claim  
6.

59. (New) A digital still camera comprising the semiconductor device according to claim 9.
60. (New) A digital still camera comprising the semiconductor device according to claim 10.
61. (New) A personal computer comprising the semiconductor device according to claim 2.
62. (New) A personal computer comprising the semiconductor device according to claim 5.
63. (New) A personal computer comprising the semiconductor device according to claim 6.
64. (New) A personal computer comprising the semiconductor device according to claim 9.
65. (New) A personal computer comprising the semiconductor device according to claim 10.
66. (New) A video camera comprising the semiconductor device according to claim 2.
67. (New) A video camera comprising the semiconductor device according to claim 5.
68. (New) A video camera comprising the semiconductor device according to claim 6.
69. (New) A video camera comprising the semiconductor device according to claim 9.

70. (New) A video camera comprising the semiconductor device according to claim 10.
71. (New) A portable phone comprising the semiconductor device according to claim 2.
72. (New) A portable phone comprising the semiconductor device according to claim 5.
73. (New) A portable phone comprising the semiconductor device according to claim 6.
74. (New) A portable phone comprising the semiconductor device according to claim 9.
75. (New) A portable phone comprising the semiconductor device according to claim 10.
76. (New) An image reproducing apparatus provided with a recording medium,  
comprising the semiconductor device according to claim 2.
77. (New) An image reproducing apparatus provided with a recording medium,  
comprising the semiconductor device according to claim 5.
78. (New) An image reproducing apparatus provided with a recording medium,  
comprising the semiconductor device according to claim 6.
79. (New) An image reproducing apparatus provided with a recording medium,  
comprising the semiconductor device according to claim 9.
80. (New) An image reproducing apparatus provided with a recording medium,  
comprising the semiconductor device according to claim 10.

81. (New) A display device comprising the semiconductor device according to claim 8.
82. (New) A digital still camera comprising the semiconductor device according to claim 8.
83. (New) A personal computer comprising the semiconductor device according to claim 8.
84. (New) A video camera comprising the semiconductor device according to claim 8.
85. (New) A portable phone comprising the semiconductor device according to claim 8.
86. (New) An image reproducing apparatus provided with a recording medium, comprising the semiconductor device according to claim 8.